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PATENT  
Attorney Docket No.: 020073-010098-96  
RECEIVED  
JUN 26 1996  
GROUP 2300

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )

Michael S. PETERS et al. )

Serial No.: 08/229,526 )

Group Art Unit: 2316

Filed: April 19, 1994 )

Examiner: St. J. Courtenay, III

For: MULTITHREADED BATCH )  
PROCESSING SYSTEM )

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants submit the following documents for the Examiner's consideration. A copy of Form PTO-1449 and of each of the documents is enclosed for the Examiner's convenience.

U.S. PATENTS

5,043,866 Myre et al.

OTHER DOCUMENTS

Datamation, vol. 38, no. 25, 12/15/92, pages 57-58, Kador, "Reengineering to boost software productivity."

Parallel Computing: Theory and Practice, Quinn, 1994 by McGraw-Hill, Inc.,  
pages 1-85.

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Designing Efficient Algorithms for Parallel Computers, Quinn, 1987, pages 13-42.

Encyclopedia of Computer Science, Ralston et al., Van Nostrand Reinhold publishing, New York, 1993, pages 1011-1025.

Each of the above-listed references was cited by the Examiner in co-pending Application Number 08/229,538.

This Information Disclosure Statement is believed to be submitted after the mailing of a first Office Action. Accordingly, a check in the amount of \$220.00 is enclosed to cover the fee required under 37 C.F.R. § 1.17(p). If any variance between the amount paid and the amount due exists, please charge the undersigned's Deposit Account No. 02-0375.

Applicants respectfully request that the above-mentioned documents be considered by the Examiner.

Respectfully submitted,

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SFP/BMB/mtr

Enclosures